



Attorney Docket No.: <u>NICHIA-00700</u>

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

| In re Application of:              | ) Group:                         |
|------------------------------------|----------------------------------|
| ••                                 | ) Art Unit:                      |
| Shuji Nakamura et al.              | ) Examiner:                      |
| •                                  | )                                |
| Serial No.: 09/463,643             | )                                |
|                                    | ) TRANSMITTAL LETTER             |
| Filed: January 25, 2000            | )                                |
| • •                                | ) 260 Sheridan Avenue, Suite 420 |
| For: NITRIDE SEMICONDUCTOR         | ) Palo Alto, CA 94306            |
| DEVICE                             | ) (650) 833-0160                 |
|                                    |                                  |
| Assistant Commissioner for Patents |                                  |

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

Enclosed please find an Information Disclosure Statement and Form PTO-1449, including copies of the references contained thereon, for filing in the U.S. Patent and Trademark Office.

The Commissioner is hereby authorized to charge any additional fee or credit overpayment to our Deposit Account No. <u>08-1275</u>. An originally executed duplicate of this transmittal is enclosed for this purpose.

Respectfully submitted,

HAVERSTOCK & OWENS LLP

Dated:  $9/8/\delta \circ$ 

Thomas B. Haverstock

Attorneys for Applicants

Reg. No.: 32,571

CERTIFICATE OF MAILING (37 CFR § 1.8(a))

I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited with the U.S. Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to the: Assistant Commissioner for Patents, Washington D.C. 20231

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Attorney Docket No.: <u>NICHIA-00700</u>

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

| In re Application of:             | ) Group Art Unit:                                     | 4-11-01  |
|-----------------------------------|---|----------|
| Shuji Nakamura et al.             | ) Examiner:   | TIFLOWER |
| Serial No.: 09/463,643            | ) <u>INFORMATION DISCLOSURE</u><br>) <u>STATEMENT</u> |          |
| Filed: January 25, 2000           | ) 260 Sheridan Avenue, Suite 420                      |          |
| For: NITRIDE SEMICONDUCTOR DEVICE | ) Palo Alto, California 94306<br>) (650)833-0160      |          |
|                                   | <u>.                                    </u>          |          |

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

The citations listed below, copies attached, may be material to the examination of the above-identified application, and are therefore submitted in compliance with the duty of disclosure defined in 37 C.F.R. §§ 1.56 and 1.97. The Examiner is requested to make these citations of official record in this application.

Applicants have become aware of the following printed publications which may be material to the examination of this application:

Japanese Publication No.: 04144294;

Japanese Publication No.: 09180998;

Japanese Publication No.: 09246651;

Japanese Publication No.: 09260772;

Japanese Publication No.: 09293935;

Japanese Publication No.: 10242565;

Japanese Publication No.: 10256645;

Japanese Publication No.: 10270792;

Japanese Publication No.: 10290027;

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**PATENT** 

Attorney Docket No.: NICHIA-00700

- Japanese Publication No.: 10294529;
- Japanese Publication No.: 10321962;
- Japanese Publication No.: 11040893;
- Japanese Publication No.: 2000-31599;
- German Publication No.: DE 196 48 955A1; and
- "InGaN/GaN/AIGaN-based laser diodes with modulation-doped strained-layer superlattices grown on an epitaxially laterally overgrown GaN substrate," Shuji Nakamura et al., Applied Physics Letter, Vol. 72, No. 2, January 12, 1998.

This Information Disclosure Statement under 37 C.F.R. §§ 1.56 and 1.97 is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that anyone or more of these citations constitutes prior art.

Respectfully submitted,

HAVERSTOCK & OWENS LLP

Dated: 9/8/60

Thomas B. Haverstock Reg. No.: 32,571

Attorneys for Applicants

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| FORM PTO-   | 0-1449 U.S. Department of Commerce  |                              | Attorney Docket No.: NICHIA-00700 Serial No.: 09/463,643 |   |                     |       |          |             |     |
|---|---|------------------------------|--|---|---------------------|-------|----------|-------------|-----|
| INFORMATION DISCLOSISTEMENT BY APPLICANT     (Use Several Sheets If Necessary)  |   |                              | Applicant: Shuji Nakamura et al.                         |   |                     |       |          |             |     |
| (37 CFR § 1.98(b))  |   |                              |  | Filing Date: January 25, 2000 Group Art Unit: |                     |       |          |             |     |
|   |   | FC                           | REIGN PATENTS C  | R PUBLISHED FOR                               | EIGN PATENT APPLICA | TIONS |          |             |     |
|   |   |                              | ·  |   |                     |       |          | Translation |     |
|   |   | Document<br>Number           | Publication Date   | Country                                       | Patent Office       | Class | Subclass | Yes         | No  |
| BWB   | AA  | 04144294                     | 5/18/92  |   | JP                  | H01S  | 3/18     | х           |     |
| 1   | AB  | 09180998                     | 7/11/97  |   | JP                  | HOIL  | 21/20    | х           |     |
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|   | OTHER DOCUMENTS (Including Author, Title, Date, Relevant Pages, Place of Publication)   |                              |  |   |                     |       |          |             |     |
|   | AR  "InGaN/GaN-based laser diodes with modulation-doped strained-layer superlattices grown on an epitaxially laterally overgrown GaN substrate," Shuji Nakamura et al., Applied Physics Letter, Vol. 72, No. 2, January 12, 1998. |                              |  |   |                     |       |          | vn GaN      |     |
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| EXAMINER: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form |   |                              |  |   |                     |       |          |             |     |

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